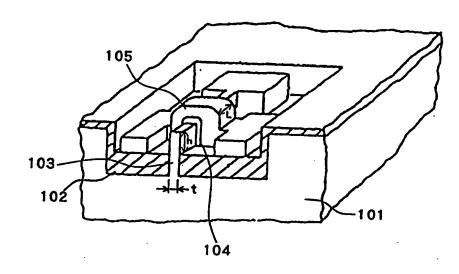
Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 1

(a)



(b)

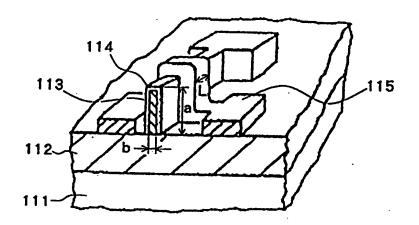
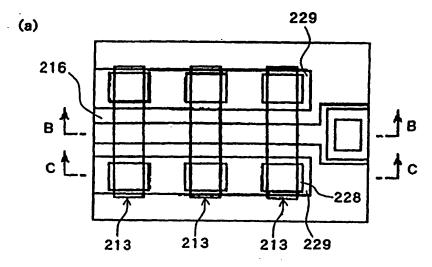
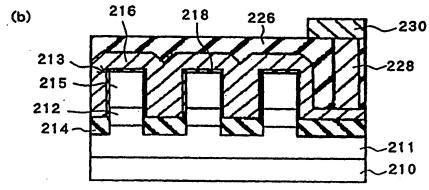
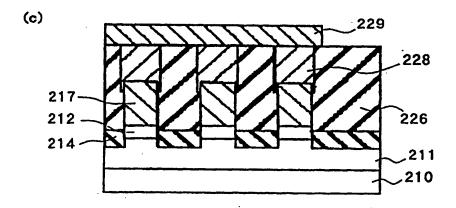


Fig. 2

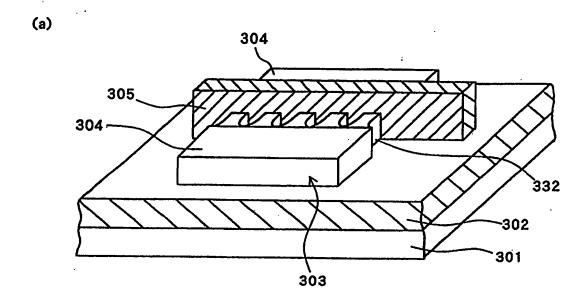


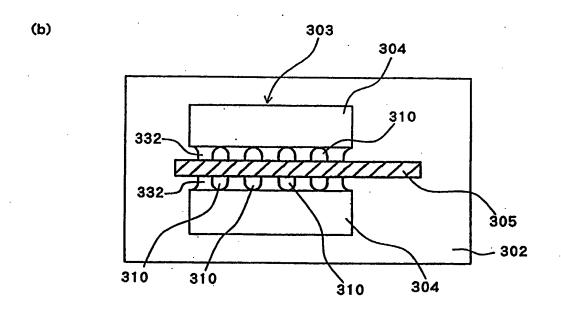




Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

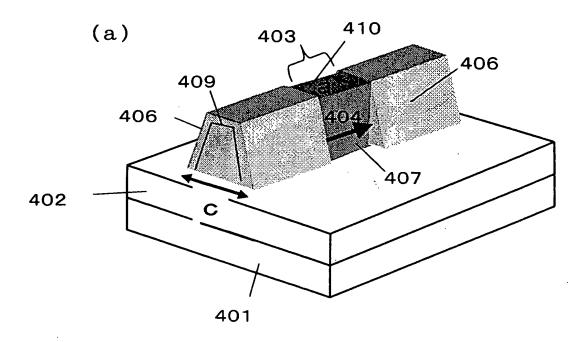
Fig. 3



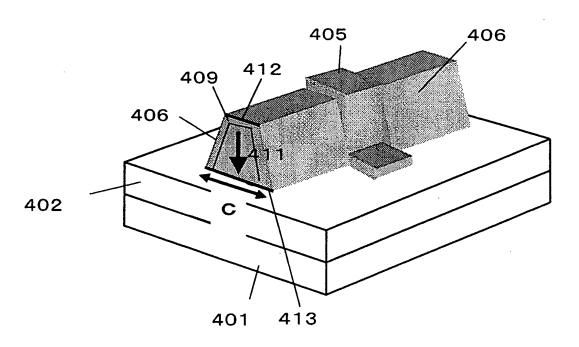


Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 4

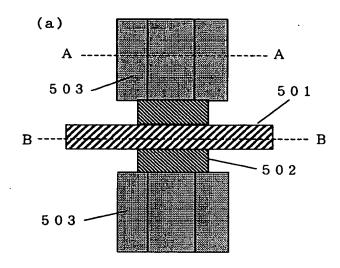


(b)



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 5



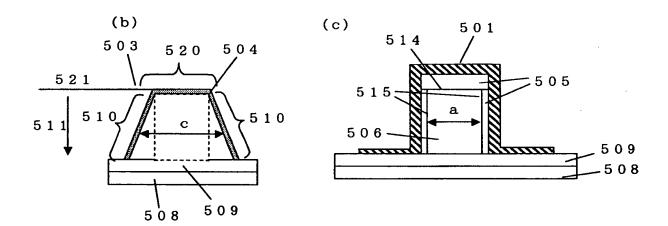


Fig. 6

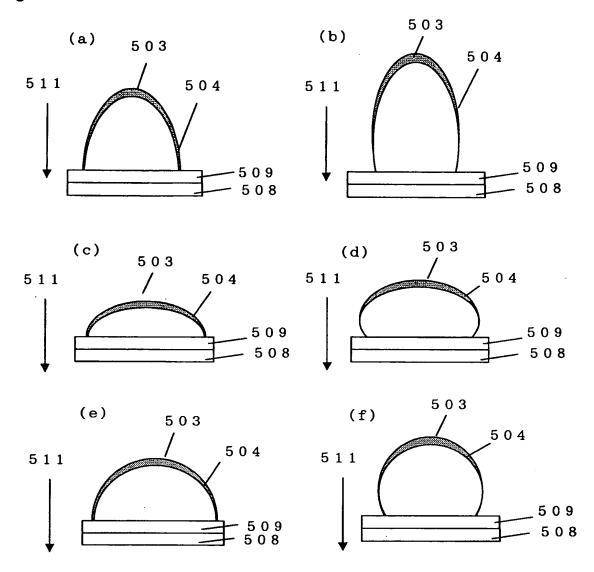
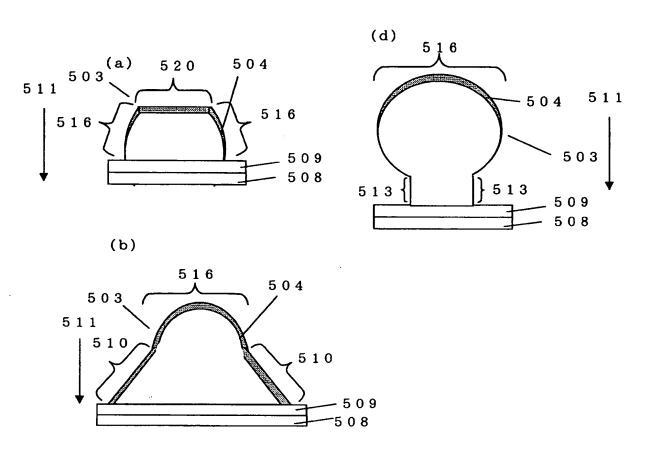
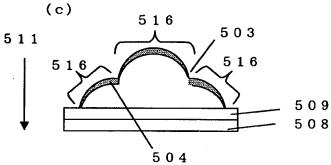


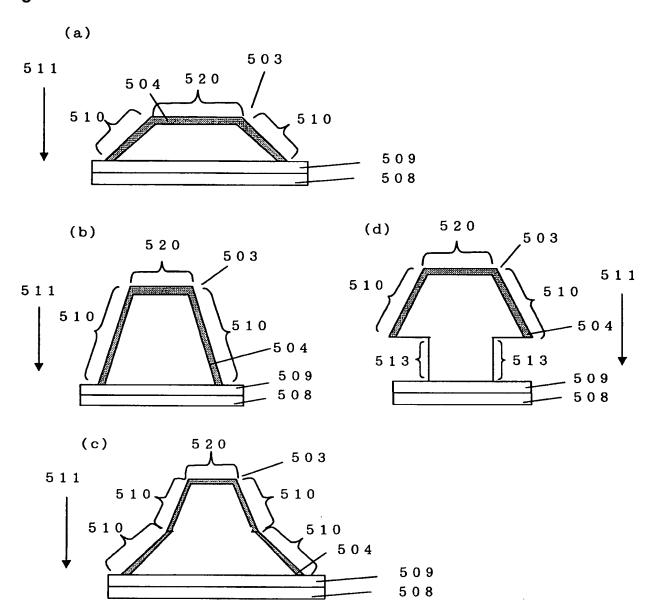
Fig. 7





Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 8



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 9

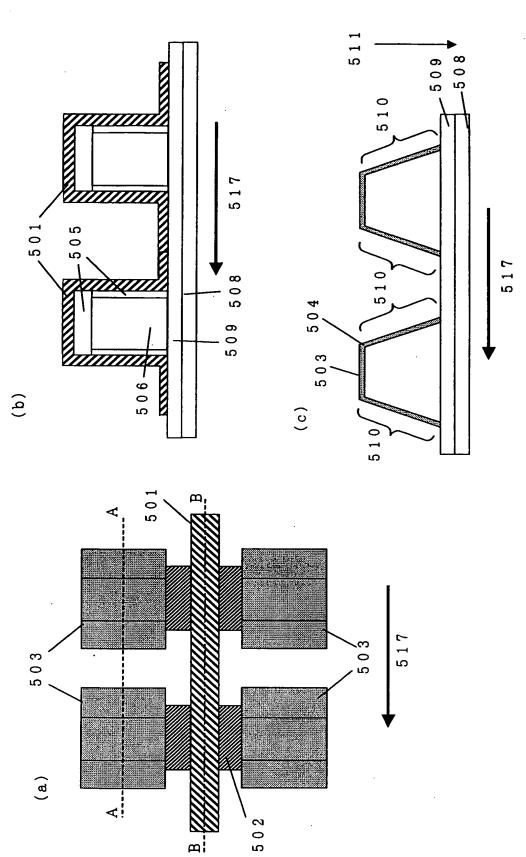
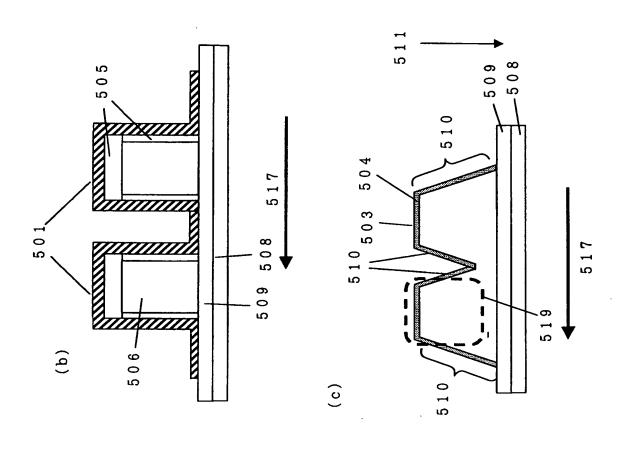


Fig. 10



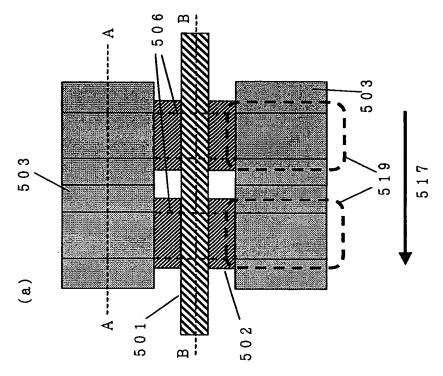
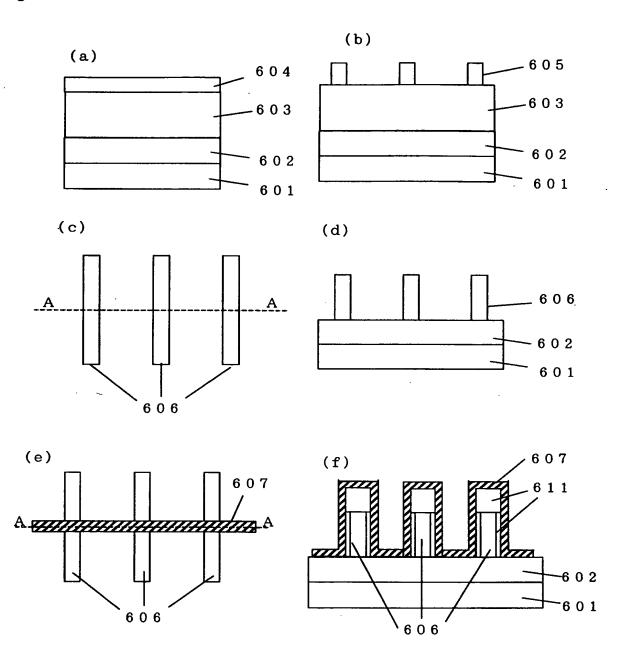


Fig. 11



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 12

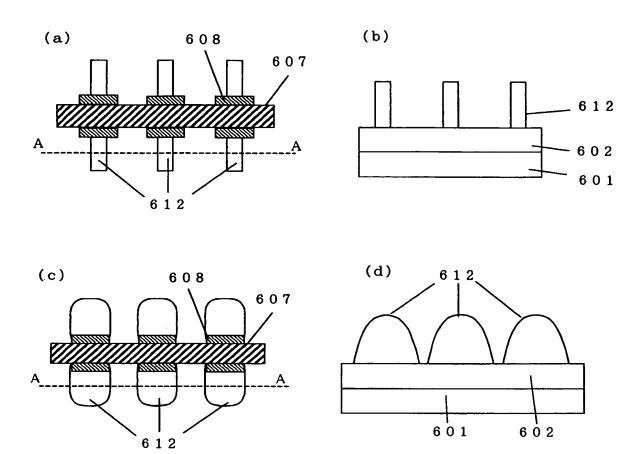
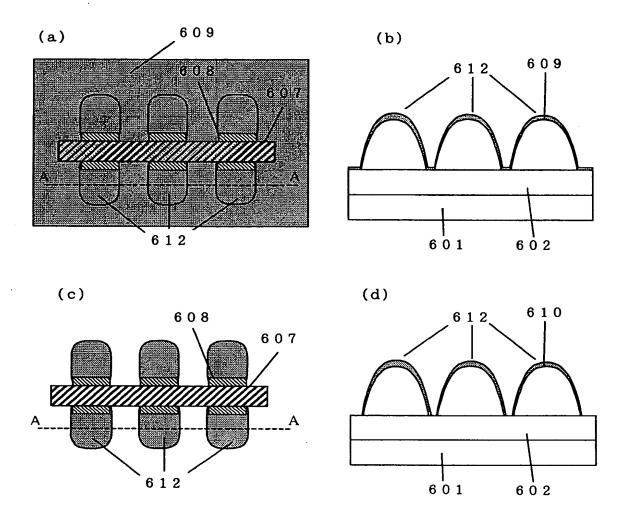


Fig. 13



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 14

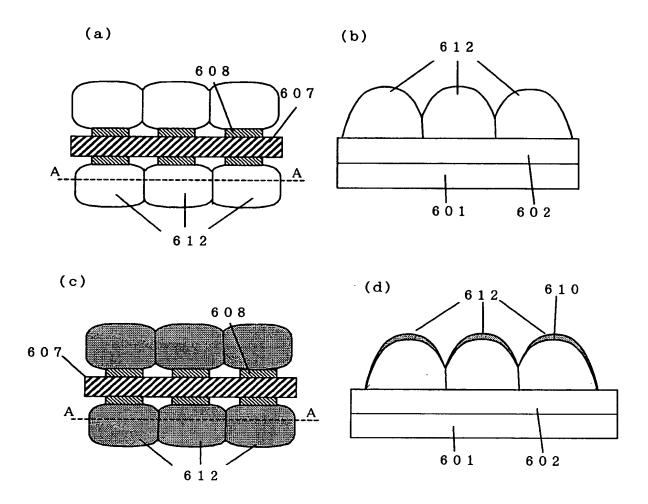
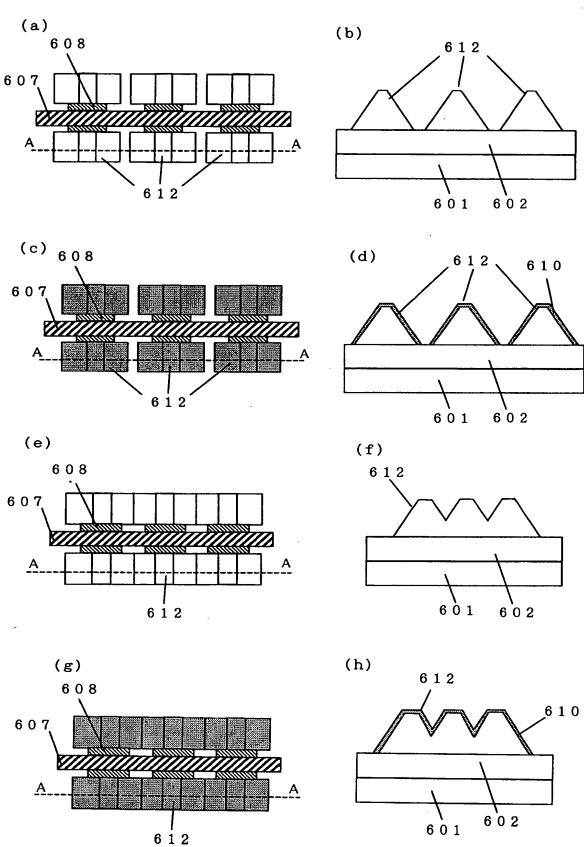


Fig. 15



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 16

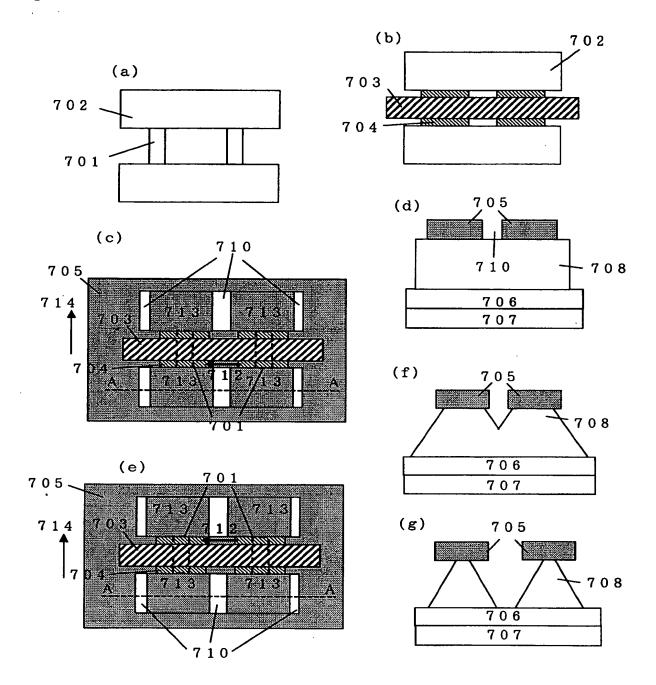
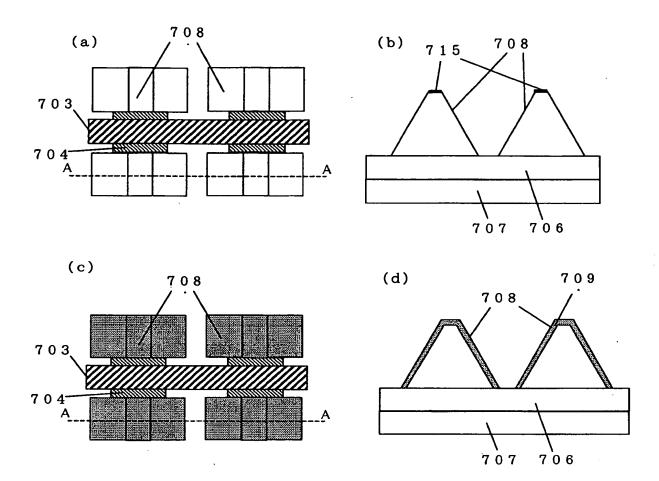
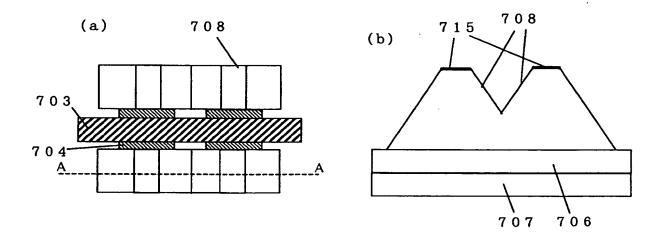


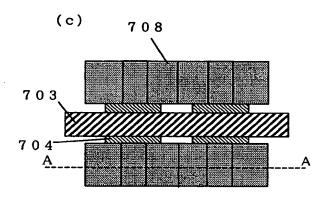
Fig. 17



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 18





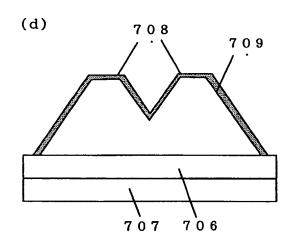
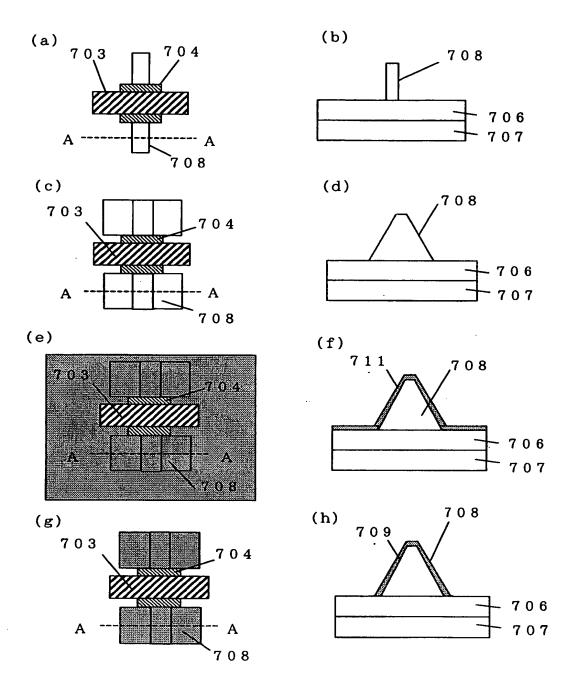
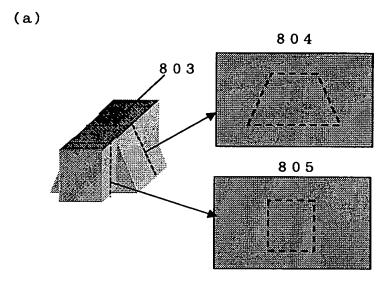


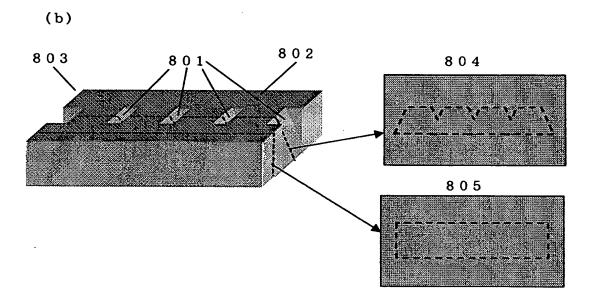
Fig. 19



Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

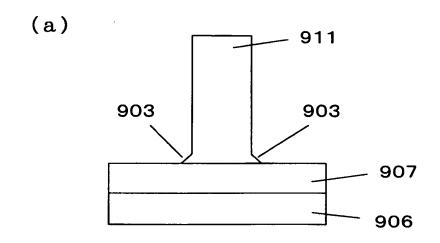
Fig. 20





Title: SEMICONDUCTOR DEVICE AND MANUFACTURING PROCESS THEREFOR

Fig. 21



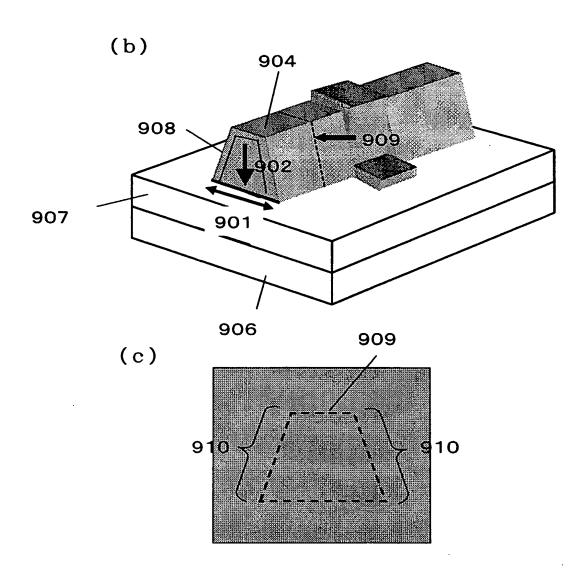


Fig. 22

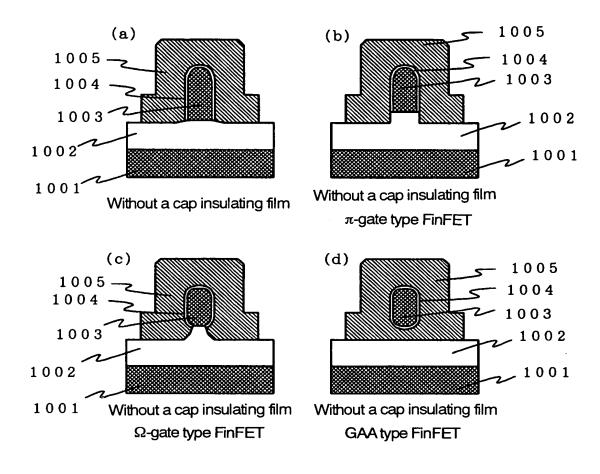


Fig. 23

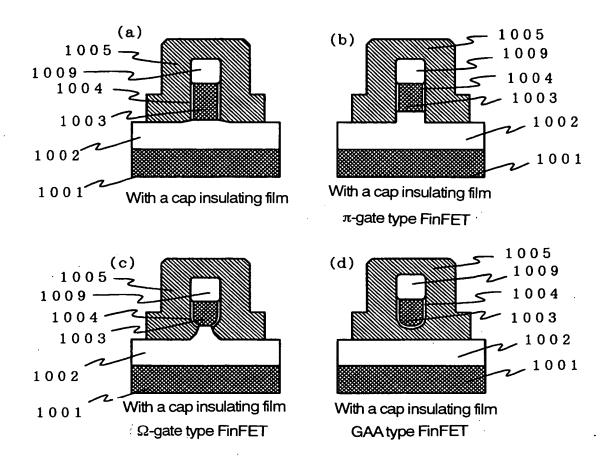
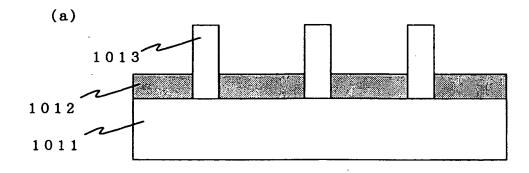
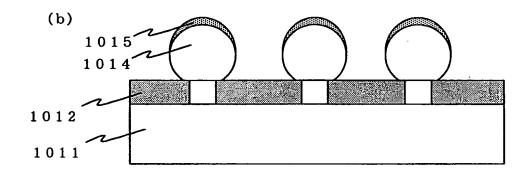


Fig. 24





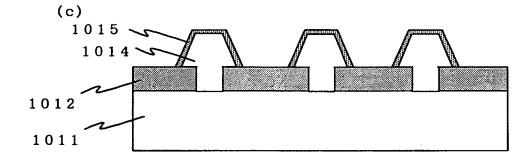


Fig. 25

